



## UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231  
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 4140

<b>SERIAL NUMBER</b> 09/879,416	<b>FILING DATE</b> 06/12/2001 <b>RULE</b>	<b>CLASS</b> 375	<b>GROUP ART UNIT</b> 2631	<b>ATTORNEY DOCKET NO.</b> L13.12-0157/01-191	
<b>APPLICANTS</b> Ian M. Flanagan, Edina, MN; Roger L. Roisen, Minnetrista, MN; <i>pl</i> Dayanand K. Reddy, Farmington, MN; Joel J. Christiansen, Independence, MN;					
<b>** CONTINUING DATA *****</b> <i>none</i> <i>pl</i>					
<b>** FOREIGN APPLICATIONS *****</b> <i>none</i> <i>pl</i>					
<b>IF REQUIRED, FOREIGN FILING LICENSE GRANTED</b> <b>** 08/08/2001</b> <i>pl</i>					
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged <i>Allowance</i> <i>pl</i> Examiner's Signature Initials		<b>STATE OR COUNTRY</b> MN	<b>SHEETS DRAWING</b> 6	<b>TOTAL CLAIMS</b> 18	<b>INDEPENDENT CLAIMS</b> 3
<b>ADDRESS</b> Leo J. Peters LSI LOGIC CORPORATION M/S D-106 1551 McCarthy Boulevard California ,CA 95035					
<b>TITLE</b> Delay-locked loop with built-in self-test of phase margin					
<b>FILING FEE RECEIVED</b> 710	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:				
		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit			